MITWELL KUBER-2000-IT Series User Manual



KUBER-2000-IT Series

User Manual

Version 2.5 2021/10/06

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1 REVISION

Revision	Date	Description
R1.8	20200107	- Update product information.
R2.0	20200430	- Update product picture
R2.2	20200511	- Update KUBER-212B GPIO & I/O rating
R2.4	20200302	- Update P13 LAN port table
		- Update P45 COM port screen shot

2 PREFACE

2.1 Copyright

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The information contained within this user manual, including but not limited to any other product specification, MiTwell, Inc reserves its right to modify them at any time without notice.

2.2 Acknowledgement

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2.3 Warranty

MiTwell warrants that each product failing to function properly under normal use for 12 months from the invoice date. Due to an effect in materials, workmanship or due to nonconformance upon specifications, will be repaired or exchanged at MiTwell's option and expense.

For RMA and DOA goods, customers should follow the standard procedure and send back the goods. For detail please see the warranty policy by below link: <u>http://rma.portwell.com.tw:10007/mitwellrma/Default.aspx</u>

2.4 Declaration of conformity

CE (European Union)

CE

This equipment is in conformity with the requirement of the following EU legislations and harmonized standards. Product also complies with the Council directions. (EN55024, EN55032)

FCC (Federal Communications Commission Radio Frequency Interface Statement)



This device complies with part 15 FCC rules. This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. (CFR Title 47 Part15 Subpart B: 2018 Class A)

Operation is subject to the following two conditions: one is the device may not cause harmful interference, and second is that the device must accept any interference received including interference that may cause undesired operation.

2.5 Technical support

MiTwell are committed to provide the best support and service for our customers, which can help customer implement and use MiTwell's products easily.

We suggest that you download the latest documentation, utilities and drivers which have been made available to assist you on MiTwell website. If you still require assistance after visiting our website, you can contact your local sales and technical support team for further assistance.

For more information on this and other products, please visit our websites at: <u>http://www.mitwell.com.tw</u>

3 INTRODUCTION

3.1 Product overview



KUBER-2000-IT series is a new generation of palm-sized, flexible and ready-to-use industrial PC designed for a variety of applications in the industrial 4.0 world. Within 10cm length, you can expect scalable Apollo Lake processors up to 4 cores, robust aluminum housing and common industrial features.

The state-of-the-art expansion design ensures various I/O choices for different applications are fulfilled with minimal investment. Leveraging KUBER-2000-IT 's Eco system, you can shorten the time to market when building your own solutions.

The product is open type, intended to be installed in and industrial control panel or an enclosure. No matter to perform data collection and transition, edge computing or communication tasks in applications like industrial automation, intralogistics or smart retailers, KUBER-2000-IT series are proved to be your best choice.

3.2 Safety Precautions

Warning note

This symbol indicates hazards that could lead to personal injury. There are three signal words indicating the severity of a potential injury. If the equipment is used in a manner not specified by the manufacturer, the protection provided by the equipment may be impaired.

	DANGER
	Indicates a hazard with a high risk level. If this hazardous
	situation is not avoided, it will result in death or serious injury.
	WARNING
$\mathbf{\Lambda}$	Indicates a hazard with a medium risk level. If this hazardous
	situation is not avoided, it could result in death or serious injury.
	CAUTION
	Indicates a hazard with a low risk level. If this hazardous situation
	is not avoided, it could result in minor or moderate injury.
	This symbol together with the NOTE signal word alerts the reader
	to a situation which may cause damage or malfunction to the
\bigcirc	device, hardware/software, or surrounding property.
	Here you will find additional information or detailed sources of
	information
	information.

3.3 Hardware Specification

Model Name	KUBER-2110-IT		KUBER-212E-IT
CPU	Intel® Celeron N3350	, 2 cores,	, 1.1GHz/2.4 GHz, 6W TDP
Mass Storage	32GB onboard eMM	C 5.0 flas	h; up to 256 GB (optional)
Memory	4GB onboard LPDDR4	w/ 2133	3MHz, up to 8GB (optional)
Power Input	24	4V VDC(+	-/-30%)
Indicator LED	Power	(Green),	Run (Green)
Operating Systems	Windows [®] 10 IoT Enterprise LT	SB 2016	(64-bit) / Linux Yocto Warrior v2.7.1
Expansion	1>	M.2 Key	r E 2230
	Basic I/O Interface		
Power Connection	n 1 x 2-pin screw-type terminal block		e terminal block
Ethernet	2 x 10/100/1000 GbE (Intel® i210)		
USB	2 x USB 3.0		
Display	1 x DP v1	.4 4096 x	< 2160 @ 60Hz
	E	nviron	ment
Operating Temperature	0°C ~ 40	°C with 0	0.5m/s air flow
Storage Temperature		-40 °C ~ 8	85 °C
Vibration	Wall mount- MIL-STD-810G 514.7	C-V Cate	gory-4 / Din Rail - 1G (DIN EN 60068-2-6)
Shock	Wall mount - 40G (EN 600	68-2-27)/	/ Din Rail - 15G (EN 60068-2-27)
Operating Altitude	Ide Up to 2000m		000m
Certification		CE, FC	cc
	Mechanical		
Degree of Protection		IP30)
Dimension (mm)	(mm) 99x 46x 92 99 x 63 x 92		99 x 63 x 92
Net Weight (Kg)	0.55		0.65
Mounting	Support DI	N rail and	d wall mounting
Cooling	Fanless		ss

Extended I/O Interface			
Model Name			
	KUBER - 212E		
Additional	51/6		
Power input (POE)	N/A		
Ethernet/ POE	N/A		
	1 x RS-232		
COM/Console	1 x RS-232/422/485		
	(BIOS Configurable)		
USB	N/A		
ı/o	N/A		
Expansion Slot	1 x M.2 Key B 2242 1 x Mini-PCle		

3.4 Appearance

3.4.1 KUBER-2110-IT



3.4.2 KUBER-212E-IT



Expanded interface with 1 x RS-232, 1 x RS-232/422/485 (BIOS Configurable)

3.5 Interface

3.5.1 USB 3.0



KUBER-2000-IT core computing part provides two USB Type-A 3.0 to connect external device such as mouse, keyboard or external storage device. Two ports are USB 3.0. USB 3.0 ports utilize a blue connector.

3.5.2 DP

DisplayPort (DP): These ports connect KUBER-2000-IT to external digital displays with a corresponding DP++ connector.

3.5.3 Ethernet

KUBER-2000-IT series are equipped with Intel[®] i210 Gigabit LAN controller, which fully compliant with 10/100/1000 Base-T Ethernet network. Two RJ45 connectors allow the computer to communicate on a 10/100/1000 Base-T Gigabit LAN Ethernet network.

SPEED ACT/LINK	Speed LED		Activity Link LED	
	Status	Description	Status	Description
	Off	10 Mbps	Off	No link
	Green	100Mbps	Green	Linked
LAN port	Orange	1 Gbps	Green(blink)	Data activity

3.5.4 LED indicator

Two LEDs are provided on the KUBER-2000-IT. These LEDs provide operating information.



Function*	Status	Description
Dowor	Power ON	Solid Green
Power	Power Off	Off
Run Status	Run	Flash Green
(GPIO)	off	Off
*: Need to auto detection functionality while power applied		

3.5.5 COM

Serial (COM): D-SUB 9 serial ports are available for use, which is configurable as either RS-232, RS-422, or RS-485 by BIOS setting. The function of the pins in the D-SUB 9 connector varies with the different configuration settings on different model, please check it on the datasheet.

D-SUB 9 pin	RS-232	RS-422	RS-485
1	DCD	TXD-	TXD-/RXD-
2	RXD	TXD+	TXD+/RXD+
3	TXD	RXD+	-
4	DTR	RXD-	-
5	GND	GND	GND
6	DSR	-	-
7	TRS	-	-
8	CTS	-	-
9	Ring indicator	-	-

3.5.6 Power terminal



A 2-position, screw-type terminal block (MC 1,5/ 2-STF-3,5) is provided for connecting power to the KUBER-2000-IT.



Connect a power source to the included power connector. This connector supports wire sizes from 0.2 to 1.5 mm² (AWG min.24). Use copper conductors only, wiring cable maximum temperature should support at least 105°C Tighten the wire to a torque value 5 kgf-cm.Torque the wire-retaining screws in the connector to 0.22Nm...0.25 Nm.

(Ce connecteur prend en charge des tailles de fil de 0,2 à 1,5 mm² (AWG min.24). Utilisez uniquement des conducteurs en cuivre, la température maximale du câble de câblage doit prendre en charge au moins 105°C Serrez le fil à un couple de 5 kgf-cm. Serrez les vis de retenue du fil dans le connecteur à 0,22 Nm... 0,25 Nm.)

Pin No.	Description
-	0 V DC
+	24VDC

3.5.7 M.2 and mini PCI-e card slot

Release the screw and disassemble the chassis of KUBER-2000-IT, you can see the M.2 and miniPCI-e card slot on the main board/carrier board. The slot varies with the different models.

3.5.8 Antenna

A mini PCIe card/M.2 2230 card slot is available to allow KUBER-2000-IT to be placed on a wireless network.



3.6 Dimensions

All dimensions shown in mm (millimeters)

3.6.1 One layer

Dimension: 99x 46x 92(mm)



Front view



Side view



Top view



3.6.2 Two layer

Dimension: 99 x 63 x 92 (mm)



Front view



Side view



Top view



4 SYSTEM INSTALLATION

KUBER-2000-IT support two types of mounting: wall mount bracket and DIN-rail mounting bracket. You can purchase mounting kit from MiTwell as an optional accessory. This chapter provide step-by-step mounting guide for the mounting options.



Make sure to fasten the screw **(M3 x 5L)** with proper torque **(5kgf-cm)** by torque wrench on KUBER-2000-IT. Improper torque may cause the damage of screw thread on the aluminum chassis.

4.1 Wall mount

Installation

Fasten the Wall mount bracket by screws (M3 x 5L) on the KUBER-2000-IT.



1. See the dimensions as below, and mark the locations of the holes on the mounting surface.



2. Use the correct anchor type for the mounting surface and securely attach the KUBER-2000-IT series IPC on the mounting surface, like metal rack. Ensure that the attaching hardware is in the small section of the keyholes.



Removal

- 1. Remove power and disconnect cables to the IPC.
- 2. Loosen and remove the screws securing the IPC to the mounting surface.



4.2 DIN-Rail mount

Fasten the DIN-rail bracket by screws **(M3 x 5L)** on the KUBER-2000-IT Suitable for 35mm (W), 7.5mm(H) DIN-rail



Install

1. Angle the KUBER-2000-IT IPC so the top edge of the mounting plate hangs on the top edge of the DIN rail.



2. Rotate the KUBER-2000-IT IPC down against the lower edge of the DIN rail.



3. Press in until the latch snaps closed.



Remove

4. Press to un-secure the device on the rail.



5. Release the latch and rotate the device away from the rail.



5 INITIAL SETUP

5.1 Chassis Grounding

KUBER-2000-IT provides good EMI protection and a stable grounding base. There is an easy-to-connect chassis grounding point to use



Chassis Grounding

5.2 Connecting Power

You can use DC power source, rated 24Vdc, if need further assistance, please contact us for further information.



5.3 External Antenna installation

Included with the installed card is an antenna (1) that attaches to a connector on the front of the unit (2). Since the KUBER-2000-IT is often installed within an enclosure, it may be advisable to install the antenna on the exterior of the enclosure with appropriate length antenna cable rather than directly to the IPC.



6 BIOS Default Setting

The Basic Input/Output System (BIOS) is a program that provides a basic level of communication between the processor and peripherals. In addition, the BIOS also contains codes for various advanced features. The default settings that work well for most configuration, most users do not need to use the BIOS setup program.

NOTE: Bios setting

Changing BIOS settings may lead to incorrect controller behavior and possible inability to boot.

BIOS options in the manual are for reference only, and are subject to the model with different configuration. Please download the latest BIOS version on the website.

6.1 Main

The Main menu displays the basic information about the system, including Project Name, BIOS version, CPU brand string, Memory information and eMMC information.

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Main Configuration Security Boot Save & Exit				
Project Name BIOS Version & Build Date EC Version & Build Date Access Level	Kuber-2000 Series 90807T00 (08/07/2019 14:01:46) 90409T00 (04/09/2019) Administrator			
Processor information Brand String	Intel(R) Atom(TM) Processor E3940 @ 1.60GHz			
Platform firmware Information BXT SOC TXE FW GOP CPU Flavor	F1 3.1.65.2317 10.0.1035 BXT Notebook/Desktop (1)			
Memory Information Total Memory Memory Speed	4096 MB (LPDDR4) 2133 MHz			
eMMC Device	MMC - DA4064(62.56B)			
System Date System Time	[Fri 01/01/2016] [00:00:30]			

Configuration	Description
System Date	This is current date setting
System Time	This is current time setting. The time is maintained by the battery when
	the device is turned off.

6.2 Configuration

Detail system configuration information, user can change configuration parameters in configuration, include CPU, Chipset, LAN, Graphics, PCI/PCIE, USB, Power Control, TPM, Super I/O, and H/W monitor.

Caution: Setting incorrect or conflicting values in configuration may cause system malfunction. Aptio Setup Utility - Copyright (C) 2019 American Megatrends, Inc. Aptio Setup Utility - Copyright (C) 2019 American Megatrends, Inc. Main Configuration Security Boot Save & Exit Chipset Configuration Chipset Configuration HAN Configuration PCD/PCIE Configuration PSuper Control Configuration Power Control Configuration Super Control Configuration Power Control Configuration Power Control Configuration Power Control Configuration H/H Monitor Intel(R) 1210 Gigabit Network Connection - 00:90:FB:6C:2B:18 Intel(R) 1210 Gigabit Network Connection - 00:90:FB:6C:2B:19

6.2.1 CPU Configuration

Aptio Configuration	Setup Utility – Copyright (C) 201	19 American Megatrends, Inc.
CPU Configuration		Number of cores to enable in each
CPU Signature	50608	processor prevence.
Niccocode Patch	10	
Max CPU Speed	1600 MHz	
Min CPU Speed	800 MHz	
Processor Cores	4	
Intel HT Technology	Not Supported	
Intel VT-x Technology	Supported	
64-bit	Supported	
L1 Data Cache	24 kB × 4	
L1 Code Cache	32 KB X 4	
L2 Cache	1024 kB x 2	
L3 Cache	Not Present	
Active Processor Cores		
Intel Virtualization Technology	[Enabled]	
VT-d	[Enabled]	
		++: Select Screen
CPU Power Management Configuration	1	t↓: Select Item
EIST	[Enabled]	Enter: Select
Turbo Mode	[Enabled]	+/-: Change Opt.
Boot performance mode	[Max Performance]	F1: General Help
C-States	[Disabled]	F2: Previous Values
Power Limit 1 Enable	[Disabled]	F3: Optimized Defaults
		F4: Save & Reset
		F12: Capture Screen
		ESC: Exit

User can change CPU configuration parameters.

Configuration	Description
Active Processor Cores	Number of cores to enable in each processor
Intel Virtualization	When enable, a VMM can utilize the additional hardware capabilities
technology	provided by Vanderpool Technology.
VT-d	Enable/Disable CPU VT-d
EIST	Enables/disables Intel SpeedStep.
Turbo Mode	Enables/disables Turbo mode.
Boot Performance Mode	Select the performance state that the BIOS will set before OS handoff,
	with option Max performance/Max Battery
C-states	Enable / Disable C state
Power Limit 1 Enable	Configure Power Limit settings

6.2.2 Chipset Configuration

Configuration	Aptio Setup Utility – Copyright (C)	2019 American Megatrends, Inc.
Chipset Configuration		Enable/Disable HD-Audio Support
HD-Audio Support SCC eMMC Support (D28:FO) eMMC Max Speed	[Enable] [Enable] [HS400]	
8254 Clock Gating	[Disable]	

Configuration Chipset feature

Configuration	Description
HD-Audio Support	Enable/Disable HD-Audio support
SCC eMMC Support	Enable/Disable SCC eMMC Support settings
(D28:F0)	
eMMC Max Speed	Select the eMMC max speed allowed
8254 Clock Gating	Enable or Disable 8254 Clock Gating

6.2.3 LAN Configuration

Apti Configuration	o Setup Utility – Copyright (C) 2019) American Megatrends, Inc.
LAN Configuration		Control the PCI Express Root Port. AUTO: To disable unused root port automatically for the most optimum
X3 LAN :		power savings.
Intel Ethernet Controller WGI210	AT	Enable: Enable PCIe root port
LAN MAC Address	00-90-FB-6C-2B-18	Disable: Disable PCIe root port
X2 LAN :		
Intel Ethernet Controller WGI210	AT	
LAN MAC Address	00-90-FB-6C-2B-19	
PCI Express Root Port 2	[Enable]	
Wake On LAN	[Disable]	
Launch UEFI PXE Rom	[Enabled]	
Iov4 PXE Support	[Enabled]	
Ipv4 HTTP Support	[Disabled]	++: Select Screen
Ipv6 PXE Support	[Enabled]	14: Select Item
Ipv6 HTTP Support	[Disabled]	Enter: Select
PXE boot wait time	0	+/-: Change Opt.
Media detect count	1	F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Reset
		F12: Capture Screen

Configuration on board LAN device

Configuration	Description
PCI Express Root Port	Control the PCI Express Root port.
Wake On Lan	Enable/Disable the wake on LAN
Launch UEFI PXE Rom	Enable/Disable UEFI Network Stack
Ipv4 PXE Support	Enable Ipv4 PXE Boot support. If disable Ipv4 boot option will not be
	created.
lpv4 Http Support	Enable Ipv4 Http support. If disable Ipv4 Http boot option will not be
	created.
Ipv6 PXE Support	Enable Ipv6 PXE Boot support. If disable Ipv6 boot option will not be
	created.
lpv6 Http Support	Enable Ipv6 Http support. If disable Ipv6 Http boot option will not be
	created.
PXE boot wait time	Wait time to press ESC key to abort the PXE boot
Media detect count	Number of times presence of media will be checked

6.2.4 Graphics Configuration

Aptio S Configuration	etup Utility – Copyright (C) 2019 American Mega	trends, Inc.
Graphics Configuration		Select DVMT 5.0 Pre-Allocated (Fixed)
		Internal Graphics Device
IGD Output Display control – GOP GOP Driver	[Enable]	
IGD Output Display control – CSM Primary IGFX Boot Display	[Auto]	

Configuration graphics settings.

Configuration	Description
DVMT Pre-Allocated	Use this item to select DVMT Pre-Allocated. Select Pre-Allocated
	(Fixed) Graphics memory size used by Internal Graphics Device from
	64M to 512M
GOP Driver	Enable GOP Driver will unload VBIOS; Disable it will load VBIOS

6.2.5 PCI/PCIE Configuration

Aptio Se	tup Utility – Copyright (C) 2019 American Mega	trends, Inc.
001112010(1011		
PCI/PCIE Configuration		Control the PCI Express Root Port.
		AUTO: To disable unused root port
PCIe Port 1 is assigned to LAN		automatically for the most optimum
PCIe Port 2 is assigned to LAN		power savings.
PCI Express Root Port 3		Enable: Enable PCIe root port
PCI Express Root Port 4		Disable: Disable PCIe root port
PCI Express Root Port 5		
PCI Express Root Port 6		
PCIE Port PCIE Port Current Config Link Width	Current Link Speed	
P1(D20/F0) ×1 ×1	GEN1 (2.5GT/s)	
P2(D20/F1) x1 x1		
P3(D19/F0) x1		
P4(D19/F1) x1		
P5(D19/F2) x1		
P6(D19/F3) x1		

PCI, PCI-X and PCI Express setting

Configuration	Description
PCIe Port Assigned to LAN	Use this item to select which PCIe Port Assigned to LAN
PCI Express root Port	Control the PCI Express Root Port 3~Port 6. Auto to disable unused
	root port automatically for the most optimum power savings.
	Enable: Enable PCIe root port, Disable: disable PCIe root port

6.2.6 USB Configuration

USB Configuration USB Controllers: 1 XHCI USB Devices: 1 Drive, 1 Keyboard SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	Configuration	Aptio Setup Utility – Copyright (C)	2019 American Megatrends, Inc.
Any USB devices plug into the connector USB Controllers: 1 XHCI USB Devices: 1 Drive, 1 Keyboard SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	USB Configuration		Enable/Disable USB port. Once disabled,
USB Port #2 [Enable] USB Port #3 [Enable]			any USB devices plug into the connector
USB Devices: 1 Drive, 1 Keyboard SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	USB Controllers:		will not be detected by Blus or US.
SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	I XHUI		
SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	USB Devices:		
SoC USB Configuration USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	I DI IVE, I KEYDOALU		
USB Port #0 [Enable] USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	SoC USB Configuration		
USB Port #1 [Enable] USB Port #2 [Enable] USB Port #3 [Enable]	USB Port #0	[Enable]	
USB Port #2 [Enable] USB Port #3 [Enable]	USB Port #1	[Enable]	
USB Port #3 [Enable]	USB Port #2	[Enable]	
	USB Port #3	[Enable]	
USB Port #4 [Enable]	USB Port #4	[Enable]	
USB Port #5 [Enable]	USB Port #5	[Enable]	
USB Port #6 [Enable]	USB Port #6	[Enable]	
USB Port #7 [Enable]	USB Port #7	[Enable]	
USB 3 Port #0 [Enable]	USB 3 Port #0	[Enable]	
USB 3 Port #1 [Enable]	USB 3 Port #1	[Enable]	
USB 3 Port #2 [Enable] ++: Select Screen	USB 3 Port #2	[Enable]	++: Select Screen
USB 3 Port #3 [Enable] 14: Select Item	USB 3 Port #3	[Enable]	14: Select Item
USB 3 Port #4 [Enable] Enter: Select	USB 3 Port #4	[Enable]	Enter: Select
USB 3 Port #5 [Enable] +/-: Change Opt.	USB 3 Port #5	[Enable]	+/-: Change Opt.
F1: General Help			F1: General Help
Common USB Configuration F2: Previous Values	Common USB Configuration		F2: Previous Values
Legacy USB Support [Enabled] F3: Optimized Defaults	Legacy USB Support	[Enabled]	F3: Optimized Defaults
USB Mass Storage Driver Support [Enabled] F4: Save & Reset	USB Mass Storage Driver Sup	oport [Enabled]	F4: Save & Reset
F12: Capture Screen	UOD handware delaws and the		F12: Capture Screen
USB handware delays and time-outs:	USB nardware delays and tin	ne-outs:	ESU: EXIT
USB transfer time-out [20 sec]	Doubles reset time out	[20 SEC]	
Device reset time-out [20 Sec]	Device reset time-out	[20 SEC]	
Desire home, oh derað [Horo]	bevice power-up detay	[Hutu]	
Mass Storage Devices:	Mass Storage Devices:		
JetFlashTranscend 2GB 8.07 [Auto]	JetFlashTranscend 2GB 8.07	[Auto]	· · · · · · · · · · · · · · · · · · ·

USB configuration setting

Configuration	Description
USB port #	Enable / Disable USB port. Once disabled, any USB devices plug into
	the connector will not be detected by BIOS or OS
Legacy USB support	Enable/Disable Legacy USB support. Auto option disables legacy
	support if no USB devices are connected. Disable option will keep
	USB devices available only for EFI applications.
USB Mass storage driver	Enable/Disable USB Mass Storage Driver support
support	
USB transfer time-out	Time-out value for control, bulk, and interrupt transfers.
Device reset time-out	USB mass storage device Start Unit command time-out.
Device power-up delay	Maximum time the device will take before properly reporting itself
	to the Hot Controller, with 'Auto' using default value, for a Root
	port 100 ms, and for a Hub port the delay is taken from the Hub
	descriptor

6.2.7 Power Control Configuration

Configuration	ptio Setup Utility – Copyright (C)	2019 American Megatrends, inc.	
Power Control Configuration		Enables or Disables System ability to	
Enable Hibernation ACPI Sleep State	[Enabled] [S3 (Suspend to RAM)]	Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.	
RTC Wakeup System Time Wake up day Wake up Time(HH:mm:ss)	[Disabled] [00:01:24] 0 [00:00:00]		

System power control configuration parameters.

Configuration	Description
Enable Hibernation	Enables/disables System ability to hibernate (OS/S4 Sleep State), this
	option may not effective with some OS.
ACPI Sleep State	Select the highest ACPI sleep state the system will enter when the
	SUSPEND button is press.
RTC Wakeup	Enables/disables system wake on alarm event

6.2.8 TPM Configuration

Aptio S Configuration	Setup Utility – Copyright (C)	2019 American Megatrends, Inc.
TPM20 Device Found		Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and
Security Device Support		INT1A interface will not be available.
Active PCR banks	SHA-1,SHA256	
Available PCR banks	SHA-1,SHA256	
SHA-1 PCR Bank	(Enabled) (Enabled)	
officer for bank	[Endo 200]	
Pending operation	[None]	
Platform Hierarchy	[Enabled]	
Storage Hierarchy	[Enabled]	
Endorsement Hierarchy	[Enabled]	
TPM2.0 UEFI Spec Version	[TCG_2]	
Physical Presence Spec Version	[1.3]	
TPM 20 InterfaceType	[TIS]	
Device Select	[Auto]	

Trusted computing settings

Configuration	Description	
Security Device support	Enable/Disable BIOS support for security device. O.S will not show	
	security device.	
Pending operation	Schedule an operation for the security device.	
TPM2.0 UEFI Spec version	Select the TCG2 spec version support, TCG_1_2: compatible mode for	
	Win8/Win10, TCG_2: support new TCG2 protocol and event format for	
	Win10 or later.	

6.2.9 Super I/O Configuration

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Configuration			
Super IO Configuration		Enable/Disable Watch Dog Timer	
LPSS HSUART #1 Support (D24:F0)	[Disable]		
LPSS HSUART #2 Support (D24:F1)	[Disable]		

System super IO chip parameters

Configuration	Description
Watch Dog Timer	Enable/Disable watch dog timer.
LPSS HSUART Support	Enable/DLAN

6.2.10 H/W Configuration

Configuration	Aptio Setup Utility – Copyright	(C) 2019 American Megatrends, Inc.
CPU Temperature	: +43 °c	
Vcore	: +0.930 V	
+3.3V	: +3.378 V	
+5V	: +5.190 V	
VDIMM	: +1.128 V	

Monitor H/W status

6.3 Security

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Main Configuration <mark>Security</mark> Boot Save & Exit			
Password Description		[Setup] check password when enter setup screen.	
If ONLY the Administrator's pass then this only limits access to only asked for when entering Set If ONLY the User's password is s is a power on password and must boot or enter Setup. In Setup th have Administrator rights. The password length must be in the following range:	word is set, Setup and is up. et, then this be entered to e User will	[Power on] check password on every time system power on.	
Minimum length Maximum length	3 20		
Password Check Mode Setup Administrator Password User Password			

Setup the security and check password when enter setup screen or when system power on.

Configuration	Description
Password check mode	Check password when enter setup screen
(Setup)	
Password check mode	Check password on every time system power on
(Power on)	

6.4 Boot

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Main Configuration Security <mark>Boot</mark> Save & Exit		
Boot Configuration Setup Promot Timeout Bootup NumLock State Post Report Summary Screen CSM Support OS Selection Full Screen Logo	2 [On] [Disabled] [Disabled] [Disabled] [Disabled]	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Boot mode select	[UEFI]	
FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	[Hard Disk] [USB Device:UEFI: JetFlashTranscend 2GB] [CD/DVD] [Network:UEFI: PXE IP4 Intel(R) I210 Gi]	
 UEFI USB Drive BBS Priorities UEFI NETWORK Drive BBS Prior 	s Ities	++: Select Screen

System boot setting and boot order priorities

Configuration	Description
Setup Prompt Timeout	Sets the number of seconds before the setup activation key is
	launched, with 65535(0xFFFF) for indefinite wait.
Bootup NumLock State	Select the keypad Number Lock states
OS selection	[Default] to Win8/8.1/10, [others] to Android, [legacy system] to
	Win7/Dos. This item setting will effect LPSS & XHCI Hand-off items
	settings.
Full screen logo	Enables or disables quiet boot option and full screen logo
Boot mode select	Select boot mode Legacy/ UEFI
UEFI Hard disk drive BBS	Specifies the boot device priority sequence from available UEFI hard
priorities	disk drives.
UEFI USB Drive BBS	Specifies the boot device priority sequence from available UEFI USB
Priorities	drives.
UEFI Network Drive BBS	Specifies the boot device priority sequence from available UEFI
Priorities	Network drives.

6.5 Save & Exit

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Main Configuration Security Boot <mark>Save & Exit</mark>			
Save Options	Reset the system after saving the		
Save Changes and Reset	changes.		
Discard Changes and Reset			
Default Options			
Restore Defaults			
Boot Override			
UEFI: JetFlashTranscend 2GB 8.07, Partition 1			
UEFI: PXE IP4 Intel(R) I210 Gigabit Network Connection			
UEFI: PXE IP4 Intel(R) I210 Gigabit Network Connection			
UEFI: PXE IP6 Intel(R) I210 Gigabit Network Connection			
UEFI: PXE IP6 Intel(R) I210 Gigabit Network Connection			
UEFI: Built-in EFI Shell			
Launch EFI Shell from filesystem device			

Configuration	Description
Save Changes and Reset	Reset the system after saving the changes.
Discard Changes and	Resets system setup without saving any changes.
Reset	
Restore defaults	Restore / load default values for all the setup options.
Launch EFI shell from	Attempts to launch EFI shell application from one of the available
filesystem device	filesystem devices.

7 MAINTENANCE

NOTE: Electrostatic discharge

Electrostatic discharge can damage or destroy components. When handling, observe the necessary safety precautions against electrostatic discharge (ESD) according to EN 61340-5-1 and IEC 61340-5-1.

The KUBER-2000-IT series can be disassembled and install the additional storage, Wifi module,...etc. by M.2 2242 slot and mini-PCIe slot.



NOTE: Failure to ensure thermal pads are properly positioned may result in performance loss.

7.1 M.2 2230 Wifi Module Installation

1 Remove 2 screws (1, 2) at the bottom of the front bracket.



2 Remove 4 screws (3, 4, 5, 6) from the rear bracket.



3 Remove the rear bracket(7).

4 Pull out the bottom heat sink (8).



- 5 Turn around the system, M.2 2230 wifi card can be installed on it.
- 6 Finishing card's installation, put the bottom heat sink back to the initial postion. Lock the front/rear bracket by screws (1, 2, 3, 4, 5, 6).





Make sure to fasten the screw back with proper torque **(5kgf-cm)** by torque wrench on KUBER-2000-IT. Improper torque may cause the damage of screw thread on the aluminum chassis.

7.2 M.2 2242 and Mini PCI-e card installation



NOTE: expansion card may various for different model, please check specification for each model.

1 Remove 2 screws (1, 2) at the bottom of the front bracket.

$\mathbf{\cdot}$	

NOTE: need to remove the screw of COM port for some specific model.



2 Remove 4 screws (3, 4, 5, 6) from the rear bracket.



3 Remove the rear bracket(7).

4 Pull out the bottom heat sink (8).



- 5 Turn around the system, you can find the miniPCI-e and M.2 2242 card slot can be installed on it.
- 6 Finishing card's installation, put the bottom heat sink back to the initial postion. Lock the front/rear bracket by screws (1, 2, 3, 4, 5, 6).





Make sure to fasten the screw back with proper torque **(5kgf-cm)** by torque wrench on KUBER-2000-IT. Improper torque may cause the damage of screw thread on the aluminum chassis.

7.3 Battery change

- 1 Follow the same procedure to open the chasses of KUBER-2000-IT. Purchase the CMOS battery kit by below P/N from MiTwell
- 2 Change and replace the battery kit.



Model Name	Part Number	Description
Battery kit	B787J890	CMOS Battery kit, Coin manganese dioxide lithium
		battery (CR2032W), 210mAh Blue W/Wire 10.0CM
		Glue:3M, operating temperature -40°C~125°C



Dispose of used batteries properly. Always adhere to currently valid national regulations for battery disposal.

A APPENDIX

A.1 Naming rule

Model Name	Part Number	Description
		Intel® Celeron N3350, 4G LPDDR4 Memory, 32G eMMC,
KUBER-212E-IT- N121	AS7-3066	24V, 2xLAN, 1xDP, 2xUSB 3.0 w/o mount kit and adapter.
		(0~50C, no throttling)
		Expended E, 2x COM, 1xMini-PCle, 1xM.2 KeyB



KUBER-2000-IT series				
Model Name Part Number		Description		
		Intel® Celeron N3350, 4G LPDDR4 Memory, 32G eMMC, -		
KUBER-2110-IT-N121	AS7-3185	24V(+/-30%), 2xLAN, 1xDP, 2xUSB 3.0 w/o mount kit and		
		adapter. (0~40C, no throttling)		
		Intel [®] Celeron N3350, 4G LPDDR4 Memory, 32G eMMC,		
KUBER-212E-IT-N121	AS7-3186	24V(+/-30%), 2xLAN, 1xDP, 2xUSB 3.0 w/o mount kit and		
		adapter. (0~40C, no throttling)		
		Expended E, 2x COM, 1xMini-PCle, 1xM.2 KeyB		

A.2 System ordering information

A.3 Accessory ordering information

Accessory		
Model Name	Part Number	Description
Battery kit	B787J890	CMOS Battery kit, Wide temperature: -40~125℃
Power adaptor	APH-8000	60W Power Adapter, 24V/2.5A , with 2pin connector
Wall mount bracket	B787J130	Wall mount bracket
DIN Rail bracket	B787J240	DIN Rail bracket
DP to VGA	B787W020	DP to VGA Video Adaptor
DP to DVI	B786W030	DP to DVI Video Adaptor

SSD (M.2 2242)				
Model Name	Part Number	Description		
32GB	B473K280	32GB M.2 2242 SSD, SATA3, MLC		
64GB	B473K290	64GB M.2 2242 SSD, SATA3, MLC (0~70°C)		
128GB	B473K880	128GB M.2 2242 SSD, SATA3, MLC (0~70°C)		
256GB	B473K890	256GB M.2 2242 SSD, SATA3, MLC (0~70°C)		
32GB Wide-Temp	B473K840	32GB M.2 2242 SSD, SATA3, MLC Wide-Temp.(-40~85°C)		
64GB Wide-Temp	B473K850	64GB M.2 2242 SSD, SATA3, MLC Wide-Temp. (-40~85℃)		
128GB Wide-Temp	B473K860	128GB M.2 2242 SSD, SATA3, MLC Wide-Temp. (-40~85℃)		
256GB Wide-Temp	B473K870	256GB M.2 2242 SSD, SATA3, MLC Wide-Temp. (-40~85℃)		
SSD Heatsink	B787J450	M.2-2242-storage Heatsink kit for KUBER-212E		

A.4 Jumper Setting

1.1 KUBER-212E-IT (PSMC-C303)



JPA1 COM1 RI Function

	Fun	ction	PIN	Signal Description	PIN	Signal Description
	1-2 Short	(12V)	1	12V	2	VCC_RI#1
Default	3-4 Short	(RI)	3	RI#1	4	VCC_RI#1
	5-6 Short	(5V)	5	5V	6	VCC_RI#1

JPB1 COM2 RI Function

	Fun	ction	PIN	Signal Description	PIN	Signal Description
	1-2 Short	(12V)	1	12V	2	VCC_RI#1
Default	3-4 Short	(RI)	3	RI#2	4	VCC_RI#2
	5-6 Short	(5V)	5	5V	6	VCC_RI#1

A.5 COM port setting

KUBER-212E-IT provides COM port on the front panel in the form of D-sub 9P connector, which support RS232/RS422/RS485 mode as set in BIOS. See the table as below for detail function

Configuration -> Super IO Configuration

Configuration		
Super IO Configuration Module Module Serial Port 1 Device Settings Transceiver Mode	[Enabled] IO=3F8h; IRQ=4; (RS-2321	Transceiver Mode Loopback RS-232
RS-422/485 Line Termination	[Disabled]	RS-485/422 Full-Duplex RS-485 Half-Duplex
Module Serial Port 2 Device Settings	<pre>[Enabled] IO=2F8h; IRQ=3;</pre>	
Configuration		
Super IO Configuration		
Module Module Serial Port 1 Device Settings Transceiver Mode RS-422/485 Line Termination RS-422/485 Line Drivers Module Serial Port 2	[Enabled] IO=3F8h; IRQ=4; [RS-485/422 Full-Ouplex] [Disabled] [Always On]	— RS-422/485 Line Drivers —— Always On Follow RTS
Device Settings	IO=2F8h; IRQ=3;	

COM configuration				
Transceiver Mode	Line drivers	Description		
RS-232	N/A	TX/RX/CTS/RTS Signal Only		
DC422/495 Full duploy	Always on	TX Enable, RX Enable		
RS422/485 Full duplex	Always on	RS-422/ RS-485 TX/RX always On		
DC422/495 Full duploy	Follow PTS	TX (Follow RTS Control), RX Enable		
K5422/485 Full duplex	FOILOW RTS	RS-422/ RS-485 TX follow RTS control		
DC48E Half duploy	Always on	Master Mode		
K5465 Hall duplex	Always off	TX Enable, RX Disable		
DC48E Half duploy	Follow PTS	Slave Mode		
R5405 Hall duplex		TX Disable, RX Enable		

A.6 Golden finger pin-out table

Open golden finger pinout help user quick buildup their own solution.



D Dim	Drimony (Top) Sido	Type /	S_Din	Casandany (Dattam) Sida	Type /
P-PIN	Primary (Top) Side	Tolerance	5-PIN	Secondary (Bottom) Side	Tolerance
P1	SMB_ALERT_1V8#	CMOS 1.8V	S1	CSI1_TX+ / I2C_CAM1_CK	CMOS 1.8V
P2	GND		S2	CSI1_TX- / I2C_CAM1_DAT	CMOS 1.8V
Р3	CSI1_CK+		S3	GND	
P4	CSI1_CK-		S4	ICH_SERIRQ	CMOS 3.3V
P5	No Connect		S5	CSI0_TX- / I2C_CAM0_CK	CMOS 1.8V
P6	No Connect		S6	CAM_MCK	CMOS 1.8V
P7	CSI1_RX0+		S7	CSI0_TX+ / I2C_CAM0_DAT	CMOS 1.8V
P8	CSI1_RX0-		S8	CSI0_CK+	
Р9	GND		S9	CSI0_CK-	
P10	CSI1_RX1+		S10	GND	
P11	CSI1_RX1-		S11	CSI0_RX0+	
P12	GND		S12	CSIO_RXO-	
P13	CSI1_RX2+		S13	GND	
P14	CSI1_RX2-		S14	CSI0_RX1+	
P15	GND		S15	CSIO_RX1-	
P16	CSI1_RX3+		S16	GND	
P17	CSI1_RX3-		S17	Co-Lay	
P18	GND		S18	Co-Lay	
P19	Co-Lay		S19	Co-Lay	
P20	Co-Lay		S20	Co-Lay	

P21	Co-Lay		S21	Co-Lay	
P22	Co-Lay		S22	Co-Lay	
P23	Co-Lay		S23	Co-Lay	
P24	Co-Lay		S24	Co-Lay	
P25	Co-Lay		S25	GND	
P26	Co-Lay		S26	Co-Lay	
P27	Co-Lay		S27	Co-Lay	
P28	Co-Lay		S28	No Connect	
P29	Co-Lay		S29	PCIE_D_TX+	
P30	Co-Lay		S30	PCIE_D_TX-	
P31	SPI0_CS1#	CMOS 1.8V	S31	Co-Lay	
P32	GND		S32	PCIE_D_RX+	
P33	SDIO_WP	CMOS 3.3V	S33	PCIE_D_RX-	
P34	SDIO_CMD	CMOS 3.3V	S34	GND	
P35	SDIO_CD#	CMOS 3.3V	S35	No Connect	
P36	SDIO_CK	CMOS 3.3V	S36	No Connect	
P37	SDIO_PWR_EN	CMOS 3.3V	S37	USB3_VBUS_DET	CMOS 5V
P38	GND		S38	AUDIO_MCK	CMOS 1.8V
P39	SDIO_D0	CMOS 3.3V	S39	I2S0_LRCK	CMOS 1.8V
P40	SDIO_D1	CMOS 3.3V	S40	I2S0_SDOUT	CMOS 1.8V
P41	SDIO_D2	CMOS 3.3V	S41	I2S0_SDIN	CMOS 1.8V
P42	SDIO_D3	CMOS 3.3V	S42	12S0_CK	CMOS 1.8V
P43	SPI0_CS0#	CMOS 1.8V	S43	No Connect	
P44	SPI0_CK	CMOS 1.8V	S44	No Connect	
P45	SPI0_DIN	CMOS 1.8V	S45	LPC_CLKOUT1	CMOS 3.3V
P46	SPI0_DO	CMOS 1.8V	S46	LFRAME_N	CMOS 3.3V
P47	GND		S47	GND	
P48	SATA_TX+		S48	I2C_GP_CK	CMOS 1.8V
P49	SATA_TX-		S49	I2C_GP_DAT	CMOS 1.8V
P50	GND		S50	HDA_SYNC / I2S2_LRCK	CMOS 1.8V
P51	SATA_RX+		S51	HDA_SDO / I2S2_SDOUT	CMOS 1.8V
P52	SATA_RX-		S52	HDA_SDI / I2S2_SDIN	CMOS 1.8V
P53	GND		S53	HDA_CK / I2S2_CK	CMOS 1.8V
P54	No Connect		S54	SATA_ACT#	CMOS 3.3V
P55	No Connect		S55	No Connect	
P56	No Connect		S56	No Connect	
P57	No Connect		S57	No Connect	

P58	No Connect		S58	No Connect	
P59	GND		S59	USB5+	
P60	USB0+		S60	USB5-	
P61	USB0-		S61	GND	
P62	USB0_EN_OC#	CMOS 3.3V	S62	No Connect	
P63	No Connect		S63	No Connect	
P64	No Connect		S64	GND	
P65	USB1+		S65	No Connect	
P66	USB1-		S66	No Connect	
P67	USB1_EN_OC#	CMOS 3.3V	S67	GND	
P68	GND		S68	USB3+	
P69	USB2+		S69	USB3-	
P70	USB2-		S70	GND	
P71	No Connect		S71	No Connect	
P72	LAD3	CMOS 3.3V	S72	No Connect	
P73	LAD2	CMOS 3.3V	S73	GND	
P74	No Connect		S74	No Connect	
			S75	No Connect	
P75	PCIE_A_RST#	CMOS 3.3V			
P76	No Connect		S76	No Connect	
P77	LAD1	CMOS 3.3V	S77	PCIE_C_RST#	CMOS 3.3V
P78	LAD0	CMOS 3.3V	S78	PCIE_C_RX+	
P79	GND		S79	PCIE_C_RX-	
P80	No Connect		S80	GND	
P81	No Connect		S81	PCIE_C_TX+	
P82	GND		S82	PCIE_C_TX-	
P83	PCIE_A_REFCK+		S83	GND	
P84	PCIE_A_REFCK-		S84	No Connect	
P85	GND		S85	No Connect	
P86	PCIE_A_RX+		S86	GND	
P87	PCIE_A_RX-		S87	No Connect	
P88	GND		S88	No Connect	
P89	PCIE_A_TX+		S89	GND	
P90	PCIE_A_TX-		S90	No Connect	
P91	GND		S91	No Connect	
P92	HDMI_D2+ /		S92	GND	
	DP1_LANE0+				

P93	HDMI_D2- / DP1_LANE0-		S93	No Connect	
P94	GND		S94	No Connect	
P95	HDMI_D1+ /		S95	No Connect	
	DP1_LANE1+				
P96	HDMI_D1- / DP1_LANE1-		S96	No Connect	
P97	GND		S97	No Connect	
P98	HDMI_D0+ /		S98	No Connect	
	DP1_LANE2+				
P99	HDMI_D0- / DP1_LANE2-		S99	No Connect	
P100	GND		S100	No Connect	
P101	HDMI_CK+ /		S101	GND	
	DP1_LANE3+				
P102	HDMI_CK- / DP1_LANE3-		S102	No Connect	
P103	GND		S103	No Connect	
P104	HDMI_HPD / DP1_HPD	CMOS 1.8V	S104	USB3_OTG_ID	CMOS 3.3V
P105	HDMI_CTRL_CK /	CMOS 1.8V	S105	No Connect	
	DP1_AUX+				
P106	HDMI_CTRL_DAT /	CMOS 1.8V	S106	No Connect	
	DP1_AUX-				
P107	No Connect		S107	LCD1_BKLT_EN	CMOS 1.8V
P107 P108	No Connect GPIO0 / CAM0_PWR#	CMOS 1.8V	S107 S108	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ /	CMOS 1.8V
P107 P108	No Connect GPIO0 / CAM0_PWR#	CMOS 1.8V	S107 S108	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+	CMOS 1.8V
P107 P108 P109	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR#	CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- /	CMOS 1.8V
P107 P108 P109	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR#	CMOS 1.8V CMOS 1.8V	S107 S108 S109	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK-	CMOS 1.8V
P107 P108 P109 P110	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST#	CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND	CMOS 1.8V
P107 P108 P109 P110 P111	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST#	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ /	CMOS 1.8V
P107 P108 P109 P110 P111	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST#	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST#	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- /	CMOS 1.8V
P107 P108 P109 P110 P111 P112	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST#	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0-	CMOS 1.8V
P107 P108 P109 P110 P111 P112 P113	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112 P113 P114	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113 \$114	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ /	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112 P113 P114	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113 \$114	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112 P113 P114 P115	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN GPIO7	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113 \$114 \$115	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+ LVDS1_1- / eDP1_TX1- /	CMOS 1.8V
P107 P108 P109 P110 P111 P112 P112 P113 P114 P115	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN GPIO7	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113 \$114 \$115	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+ LVDS1_1- / eDP1_TX1- / DSI1_D1-	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112 P113 P114 P115 P116	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN GPIO7 GPIO8	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	\$107 \$108 \$109 \$110 \$111 \$112 \$113 \$114 \$115 \$116	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+ LVDS1_1- / eDP1_TX1- / DSI1_D1- LCD1_VDD_EN	CMOS 1.8V
P107 P108 P109 P110 P111 P111 P112 P113 P114 P115 P116 P117	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO5 / PWM_OUT GPIO6 / TACHIN GPIO7 GPIO8 GPIO9	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	S107 S108 S109 S110 S111 S112 S113 S114 S115 S116 S117	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+ LVDS1_1- / eDP1_TX1- / DSI1_D1- LCD1_VDD_EN LVDS1_2+ / eDP1_TX2+ /	CMOS 1.8V
P107 P108 P109 P110 P111 P112 P112 P113 P114 P115 P116 P117	No Connect GPIO0 / CAM0_PWR# GPIO1 / CAM1_PWR# GPIO2 / CAM0_RST# GPIO3 / CAM1_RST# GPIO4 / HDA_RST# GPIO5 / PWM_OUT GPIO6 / TACHIN GPIO7 GPIO8 GPIO9	CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V CMOS 1.8V	S107 S108 S109 S110 S111 S112 S113 S114 S115 S116 S117	LCD1_BKLT_EN LVDS1_CK+ / eDP1_AUX+ / DSI1_CLK+ LVDS1_CK- / eDP1_AUX- / DSI1_CLK- GND LVDS1_0+ / eDP1_TX0+ / DSI1_D0+ LVDS1_0- / eDP1_TX0- / DSI1_D0- No Connect LVDS1_1+ / eDP1_TX1+ / DSI1_D1+ LVDS1_1- / eDP1_TX1- / DSI1_D1- LCD1_VDD_EN LVDS1_2+ / eDP1_TX2+ / DSI1_D2+	CMOS 1.8V

				DSI1_D2-	
P119	GPIO11	CMOS 1.8V	S119	GND	
P120	GND		S120	LVDS1_3+ / eDP1_TX3+ /	
				DSI1_D3+	
P121	I2C_PM_CK	CMOS 1.8V	S121	LVDS1_3-/eDP1_TX3-/	
				DSI1_D3-	
P122	I2C_PM_DAT	CMOS 1.8V	S122	LCD1_BKLT_PWM	CMOS 1.8V
P123	No Connect		S123	RSVD	
P124	No Connect		S124	GND	
P125	BOOT_SEL2#	CMOS 1.8V	S125	LVDS0_0+ / eDP0_TX0+ /	
				DSI0_D0+	
P126	RESET_OUT#	CMOS 1.8V	S126	LVDS0_0- / eDP0_TX0- /	
				DSI0_D0-	
P127	RESET_IN#	CMOS 1.8V	S127	LCD0_BKLT_EN	CMOS 3.3V
P128	POWER_BTN#	CMOS 3.3V	S128	LVDS0_1+ / eDP0_TX1+ /	
				DSI0_D1+	
P129	SER0_TX	CMOS 1.8V	S129	LVDS0_1- / eDP0_TX1- /	
				DSI0_D1-	
P130	SER0_RX	CMOS 1.8V	S130	GND	
P131	SERO_RTS#	CMOS 1.8V	S131	LVDS0_2+ / eDP0_TX2+ /	
				DSI0_D2+	
P132	SER0_CTS#	CMOS 1.8V	S132	LVDS0_2- / eDP0_TX2- /	
				DSI0_D2-	
P133	GND		S133	LCD0_VDD_EN	CMOS 3.3V
P134	SER1_TX	CMOS 1.8V	S134	LVDS0_CK+ / eDP0_AUX+ /	
				DSI0_CLK+	
P135	SER1_RX	CMOS 1.8V	S135	LVDS0_CK- / eDP0_AUX- /	
				DSI0_CLK-	
P136	SER2_TX	CMOS 1.8V	S136	GND	
P137	SER2_RX	CMOS 1.8V	S137	LVDS0_3+ / eDP0_TX3+ /	
				DSI0_D3+	
P138	SER2_RTS#	CMOS 1.8V	S138	LVDS0_3- / eDP0_TX3- /	
				DSI0_D3-	
P139	SER2_CTS#	CMOS 1.8V	S139	I2C_LCD_CK	CMOS 3.3V
P140	SER3_TX	CMOS 1.8V	S140	I2C_LCD_DAT	CMOS 3.3V
P141	SER3_RX	CMOS 1.8V	S141	LCD0_BKLT_PWM	CMOS 1.8V
P142	GND		S142	RSVD	
P143	No Connect		S143	GND	

P144	No Connect		S144	eDP0_HPD	CMOS 1.8V
P145	No Connect		S145	WDT_TIME_OUT#	CMOS 1.8V
P146	No Connect		S146	PCIE_WAKE#	CMOS 1.8V
P147	VDD_IN	Power In	S147	VDD_RTC	CMOS 3V
P148	VDD_IN	Power In	S148	LID#	CMOS 1.8V
P149	VDD_IN	Power In	S149	SLEEP#	CMOS 1.8V
P150	VDD_IN	Power In	S150	VIN_PWR_BAD#	CMOS
					VDD_IN
P151	VDD_IN	Power In	S151	CHARGING#	CMOS 1.8V
P152	VDD_IN	Power In	S152	CHARGER_PRSNT#	CMOS 1.8V
P153	VDD_IN	Power In	S153	CARRIER_STBY#	CMOS 1.8V
P154	VDD_IN	Power In	S154	CARRIER_PWR_ON	CMOS 1.8V
P155	VDD_IN	Power In	S155	FORCE_RECOV#	CMOS 1.8V
P156	VDD_IN	Power In	S156	BATLOW#	CMOS 3.3V
			S157	No Connect	
			S158	GND	

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MiTwell, Inc. ADD: 9F, No. 242, Bo-Ai Street, Shulin Dist., New Taipei City 238, Taiwan (R.O.C.) TEL: +886 2 7739-5566 Email: info@MiTwell.com.tw Website: http:// www.MiTwell.com.tw

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ADD: No. 242, Bo-Ai Street, Shulin Dist., New Taipei City 238, Taiwan (R.O.C.) TEL: +886 2 7739-5566 Please follow RMA instruction by below link to send back the products for repairing or replacement. WEB: http://rma.portwell.com.tw:10007/mitwellrma/